

SEP 30 2005

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Simar et al

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

Filed: October 31, 2000

Docket: TI-30556

For: VERY LONG INSTRUCTION WORD MICROPROCESSOR WITH EXECUTION
PACKET SPANNING TWO OR MORE FETCH PACKETS WITH PRE-DISPATCH
INSTRUCTION SELECTION FROM TWO LATCHES ACCORDING TO
INSTRUCTION BIT (amended title)

AMENDMENT UNDER 37 C.F.R. 1.116


Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

CERTIFICATION OF FAX TRANSMITTAL
UNDER 37 C.F.R. §1.6(b)

I hereby certify that the above
correspondence is being facsimile
transmitted to the Patent and Trademark
Office on September 30, 2005.


Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response
to the Examiner's Office Action dated August 3, 2005. These
amendments are respectfully submitted as a full and complete
response to that Action.

Please amend the above referenced application as follows:

-- Do Not Enter --
009 10-11-05